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P. 01

PART B—ISSUE FEE TRANSMITTAL

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June 30, 2005

Date

Kevin L. Daffer

APPLICATION NO.	FILING DATE	FIRST NAMES INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/846,119	04/30/2001	Yitzhak Gilboa	5298-04700	5235

TITLE OF INVENTION: METHOD OF MAKING A PLANARIZED SEMICONDUCTOR STRUCTURE

APPN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1,400.00	\$0.00	\$1,400.00	07/08/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
Goudreau, George A.	1763	438-700000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

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1 Kevin L. Daffer

2 Mollie E. Lettang

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(A) NAME OF ASSIGNEE: Cypress Semiconductor Corp.

(B) RESIDENCE (CITY & STATE OR COUNTRY): San Jose, CA

Please check the appropriate assignee category indicated below (will not be printed on the patent): individual corporation or other private group entity government

4a. The following fees are enclosed:

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Issue Fee

A check in the amount of the fee(s) is enclosed.

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(Attorney or Signature)

Kevin L. Daffer
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(Date)

6/30/05

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02 FC:1501